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Attorney Docket No. 2102487-991150

Sheet 1 of 1

Form PTO-1449 (modified 2/91)	U.S. DEPT. OF COMMERCE Patent and Trademark Off.	Attorney Docket Number: 2102487-991180	Serial Number: Not yet assigned
INFORMATION DISCLOSURE CITATION (Use several sheets if necessary)		Applicant: Mikio Shiraishi	
		Filing date: September 25, 2001	Group art unit: Not yet assigned
		PC 821 U.S. PTO 09/964081 09/25/01	

U.S. PATENT DOCUMENTS

Examiner Initial	Patent number	Date	Name	Class	Sub-class	Filing date if appropriate
<i>[Signature]</i>	4,215,415	07/29/80	Kanemasa et al.			
<i>[Signature]</i>	4,879,677	11/07/89	Shiraishi			

FOREIGN PATENT DOCUMENTS

	Document number	Date	Country	Class	Sub-class	Translation
						YES NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>[Signature]</i>	IEEE Journal of Solid-State Circuits, "A Subscriber Digital Signal Processor LSI for PCM Applications", Vol. 23, No. 3, page 838, Fig. 4; K. Yamakido et al.; June 1988.

Examiner:	<i>D. H. Maltzman</i>	Date Considered:	<i>8/3/04</i>
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